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**1 Wavelength routing of 40 Gbit/s packets with 2.5 Gbit/s header erasure/rewriting using all-fibre wavelength converter**

*Olsson, B.-E.; Ohlen, P.; Rau, L.; Rossi, G.; Jerphagnon, O.; Doshi, R.; Humphries, D.S.; Blumenthal, D.J.; Kaman, V.; Bowers, J.E.*

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**2 MSXmin: a modular multicast ATM packet switch with low delay and hardware complexity**

*Kannan, R.; Ray, S.*

Networking, IEEE/ACM Transactions on , Volume: 8 Issue: 3 , June 2000

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*Mayer, A.; Ofek, Y.; Yung, M.*

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**5 Reconfigurable optical packet header recognition and routing using time-to-wavelength mapping and tunable fiber Bragg gratings for correlation decoding***Cardakli, M.C.; Lee, S.; Willner, A.E.; Grubsky, V.; Starodubov, D.; Feinberg, J.*

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**and demonstrators in the KEOPS project**

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network service for protecting e-commerce and other  
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the routes used by these demands, and evaluate traffic granularity  
levels that are attractive for improving the poor load balancing that  
our study reveals. The data used in this study was collected at a  
major POP in a commercial Tier-1 IP backbone. In the first part of  
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T. V. Lakshman , D. Stiliadis  
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Tammo Spalink , Scott Karlin , Larry Peterson , Yitzchak Gottlieb  
ACM SIGOPS Operating Systems Review , Proceedings of the 18th symposium on Proceedings of the 18th ACM symposium on operating systems principles October 2001

**Volume 35 Issue 5**


Recent efforts to add new services to the Internet have increased interest in software-based routers that are easy to extend and evolve. This paper describes our experiences using emerging network processors---in particular, the Intel IXP1200---to implement a router. We show it is possible to combine an IXP1200 development board and a PC to build an inexpensive router that forwards minimum-sized packets at a rate of 3.47Mpps. This is nearly an order of magnitude faster than existing pure PC-base ...

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RSVP has been designed to support resource reservation in the Internet. However, it has two major problems: complexity and scalability. The former results in large message processing overhead at end systems and routers, and inefficient firewall processing at the edge of the network. The latter implies that in a backbone environment, the amount of bandwidth consumed by refresh messages and the storage space that is needed to support a large number of flows at a router are too large. We have devel ...

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The use of scheduling mechanisms like Class Based Queueing (CBQ) is expected to play a key role in next generation multiservice IP networks. In this paper we attempt an experimental evaluation of ALTQ/CBQ demonstrating its sensitivity to a wide range of parameters and link layer driver design issues. We pay attention to several CBQ internal parameters that affect performance drastically and particularly to "borrowing", a key feature for flexible and efficient link sharing. We are also investigat ...

**16 Traffic characterization: Characteristics of fragmented IP traffic****80%** on internet links

Colleen Shannon , David Moore , k claffy

Proceedings of the First ACM SIGCOMM Workshop on Internet

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Fragmented IP traffic is a unique component of the overall mix of traffic on the Internet. Many assertions about the nature and extent of fragmented traffic are anecdotal rather than empirical. In this paper we examine the causes and attributes of measured

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✎ Alain J. Roy , Ian Foster , William Gropp , Brian Toonen , Nicholas Karonis , Volker Sander

Proceedings of the 2000 conference on Supercomputing November 2000

Parallel programmers typically assume that all resources required for a program's execution are dedicated to that purpose. However, in local and wide area networks, contention for shared networks, CPUs, and I/O systems can result in significant variations in availability, with consequent adverse effects on overall performance. We describe a new message-passing architecture, MPICH-GQ, that uses quality of service (QoS) mechanisms to manage contention and hence improve performance of message ...

**20 The click modular router** 80%

✎ Eddie Kohler , Robert Morris , Benjie Chen , John Jannotti , M. Frans Kaashoek

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Clicks is a new software architecture for building flexible and configurable routers. A Click router is assembled from packet processing modules called elements. Individual elements implement simple router functions like packet classification, queuing, scheduling, and interfacing with network devices. A router configurable is a directed graph with elements at the vertices; packets flow along the edges of the graph. Several features make individual elements more powerful and ...



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